

		500 250	1000 + 10/-0 500 + 10/-0	2361349-2 2361349-1
		(″ M ″)	"L"	TE P/N
THIS DRAWING IS A C		DWN 29AUG2019 B. MATTHEWS CHK 29AUG2019 R. HENRY	E TE	TE Connectivity
DIMENSIONS: 	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013	APVD - NAME 	SLIVER 2.0 2C 33AV STRAIGHT TO RIGHT	
MATERIAL -	4 PLC ±0.0001 ANGLES ±- FINISH -	weight	cage code drawing no 200779 C-2361349 scale	RESTRICTED TO -

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			P1			P2		
		PIN NO.	PAIR NO.	DESIGNATION	WIRE TYPE	DESIGNATION	PAIR NO.	PIN NO
D	Г	- A 1		GROUND	DISCRETE	GROUND		A 1
		A 2		GROUND	DISCRETE	GROUND		A 2
		- A 3		GROUND	DISCRETE	GROUND		A 3
		- A 4		GROUND	DISCRETE	GROUND		A 4
		- A 5		GROUND	DISCRETE	GROUND		A 5
		- A 6		GROUND	DISCRETE	GROUND		A 6
		Α7		SIDE BAND	DISCRETE	SIDE BAND		A 7
		A 8		SIDE BAND	DISCRETE	SIDE BAND		A 8
		A 9		SIDE BAND	DISCRETE	SIDE BAND		A 9
		A 1 0		SIDE BAND	DISCRETE	SIDE BAND		A 1 0
		A 1 1		SIDE BAND	DISCRETE	SIDE BAND		A 1 1
		A 1 2		SIDE BAND	DISCRETE	SIDE BAND		A 1 2
<u> </u>		A 1 3		GND		GND		A 1 3
		A 1 4		CLK	DIFF PAIR	CLK		A 1 4
		A 1 5		CLK	DIFF PAIR	CLK		A 1 5
		A 1 6		GND		GND		A 1 6
		A 1 7	0	РСІеТХ	DIFF PAIR	РСІеТХ	0	A 1 7
		A 1 8	0	PCIe TX	DIFF PAIR	PCIe TX	0	A 1 8
		A 1 9		GND		GND		A 1 9
		A 2 0	1	РСІеТХ	DIFF PAIR	РСІеТХ	1	A 2 0
		A 2 1	1	PCIe TX	DIFF PAIR	PCIe TX	1	A 2 1
		A 2 2		GND		GND		A 2 2
		A 2 3	2	РСІеТХ	DIFF PAIR	РСІеТХ	2	A 2 3
		A 2 4	2	PCIe TX	DIFF PAIR	PCIe TX	2	A 2 4
		A 2 5		GND		GND		A 2 5
}		A 2 6	3	РСІеТХ	DIFF PAIR	РСІеТХ	3	A 2 6
		A 2 7	3	PCIe TX	DIFF PAIR	PCIe TX	3	A 2 7
		A 2 8		GND		GND		A 2 8
				KEY		ΚΕΥ		
		A 2 9		GND		GND		A 2 9
		A 3 0	4	РСІеТХ	DIFF PAIR	РСІеТХ	4	A 3 0
		A 3 1	4	PCIe TX	DIFF PAIR	PCIe TX	4	A 3 1
		A 3 2		GND		GND		A 3 2
		A 3 3	5	РСІеТХ	DIFF PAIR	РСІеТХ	5	A 3 3
		A 3 4	5	PCIe TX	DIFF PAIR	PCIe TX	5	A 3 4
		A 3 5		GND		GND		A 3 5
		A 3 6	6	РСІеТХ	DIFF PAIR	РСІеТХ	6	A 3 6
		A 3 7	6	PCIe TX	DIFF PAIR	PCIe TX	6	A 3 7
		A 3 8		GND		GND		A 3 8
A .		A 3 9	7	РСІеТХ	DIFF PAIR	РСІеТХ	7	A 3 9
		A 4 0	7	PCIe TX	DIFF PAIR	PCIe TX	7	A 4 0
		A 4 1		GND		GND		A 4 1
		A 4 2		SIDE BAND	DISCRETE	SIDE BAND		A 4 2

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REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

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_	THIS DRAWING IS A	CONTROLLED DOCUMENT.	DWN 29AUG2019 B. MATTHEWS CHK 29AUG2019 R. HENRY	9 9 TE Connectiv	i † y
	DIMENSIONS: MM	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013	R. HENKI APVD - - PRODUCT SPEC - APPLICATION SPEC	- SLIVER 2.0 2C 33AWG 100 OHM STRAIGHT TO RIGHT ANGLE -	
	MATERIAL -	4 PLC ±0.0001 ANGLES ±- FINISH - -	weight	SIZE CAGE CODE DRAWING NO A 2 0 0 7 7 9 C - 2 3 6 1 3 4 9 SCALE 1 1 SHEET 2 0	F 3 REV R

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	P1				P2			
	PIN NO.	PAIR NO.	DESIGNATION	WIRE TYPE	DESIGNATION	PAIR NO.	PIN NO	
Г	- B 1		12V POWER	DISCRETE	12V POWER		B 1	
-	B2		12V POWER	DISCRETE	12V POWER		B 2	
	- ВЗ		12V POWER	DISCRETE	12V POWER		В 3	
	- В4		12V POWER	DISCRETE	12V POWER		Β4	
_	В 5		12V POWER	DISCRETE	12V POWER		В 5	
	В6		12V POWER	DISCRETE	12V POWER		Β6	
	В 7		SIDE BAND	DISCRETE	SIDE BAND		В7	
	Β8		SIDE BAND	DISCRETE	SIDE BAND		B8	
	В 9		SIDE BAND	DISCRETE	SIDE BAND		В9	
	B10		SIDE BAND	DISCRETE	SIDE BAND		B10	
	B 1 1		3.3 AUX POWER	DISCRETE	3.3 AUX POWER		B 1 1	
	B 1 2		SIDE BAND	DISCRETE	SIDE BAND		B12	
	B 1 3		GND		GND		B13	
	B 1 4		CLK	DIFF PAIR	CLK		B 1 4	
	B 1 5		CLK	DIFF PAIR	CLK		B15	
	B16		GND		GND		B16	
	B 1 7	0	PCIe RX	DIFF PAIR	PCIe RX	0	B 1 7	
	B18	0	PCIe RX	DIFF PAIR	PCIe RX	0	B18	
	B19		GND		GND		B19	
	B 2 0	1	PCIe RX	DIFF PAIR	PCIe RX	1	B 2 0	
	B 2 1	1	PCIe RX	DIFF PAIR	PCIe RX	1	B 2 1	
	B 2 2		GND		GND		B 2 2	
	B 2 3	2	PCIe RX	DIFF PAIR	PCIe RX	2	B 2 3	
	B 2 4	2	PCIe RX	DIFF PAIR	PCIe RX	2	B 2 4	
	B 2 5		GND		GND		B 2 5	
	B 2 6	3	PCIe RX	DIFF PAIR	PCIe RX	3	B 2 6	
	B 2 7	3	PCIe RX	DIFF PAIR	PCIe RX	3	B 2 7	
	B 2 8		GND		GND		B 2 8	
			KEY		KEY			
	B 2 9		GND		GND		B 2 9	
	B 3 0	4	PCIe RX	DIFF PAIR	PCIe RX	4	B 3 0	
	B 3 1	4	PCIe RX	DIFF PAIR	PCIe RX	4	B 3 1	
	B 3 2		GND		GND		B 3 2	
	B 3 3	5	PCIe RX	DIFF PAIR	PCIe RX	5	B 3 3	
	B 3 4	5	PCIe RX	DIFF PAIR	PCIe RX	5	B 3 4	
	B 3 5		GND		GND		B 3 5	
	B 3 6	6	PCIe RX	DIFF PAIR	PCIe RX	6	B36	
	B 3 7	6	PCIe RX	DIFF PAIR	PCIe RX	6	B 3 7	
	B 3 8	<u> </u>	GND		GND	~	B 3 8	
	B 3 9	7	PCIe RX	DIFF PAIR	PCIe RX	7	B 3 9	
	B 4 0	7	PCIe RX	DIFF PAIR	PCIe RX	7	B 4 0	
	B 4 0		GND		GND		B 4 0	
	B 4 2		SIDE BAND	DISCRETE	SIDE BAND		B 4 2	

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REVISIONS

Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

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COMMONED

THIS DRAWING IS A (CONTROLLED DOCUMENT.	DWN 29AUG2019 <u>B. MATTHEWS</u> CHK 29AUG2019 R. HENRY	TE Connectivity
	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013	R. HENKI APVD - - PRODUCT SPEC - APPLICATION SPEC	SLIVER 2.0 2C 33AWG 100 OHM STRAIGHT TO RIGHT ANGLE -
MATERIAL	4 PLC ±0.0001 ANGLES ±- FINISH - -	 weight CUSTOMER DRAWING	SIZE CAGE CODE DRAWING NO RESTRICTED A 2 0 0 7 7 9 C=2361349 - SCALE 1:1 SCALE 1:1

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