

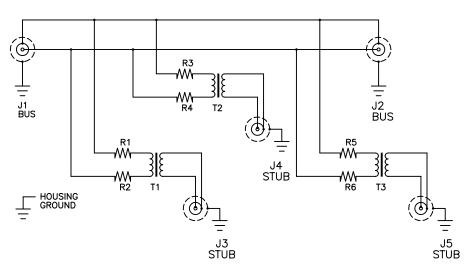
1.0 CONNECTOR PART NUMBERS

	BUS	STUB			
D-500-0255	JACKS	JACKS			
	J1 AND J2	J3 THRU J5			
-533-1-MHI	DK-621-0434-1S "A"	DK-621-0440-4P "D"			
	(8912004-00-05)	(8912004-00-04)			

2.0 <u>INTERMATEABILITY</u>

CONNECTOR BOX JACK	MATING PLUG	KEY ("X")
	DK-621-0433-1P (8912003-00-01)	Α
	DK-621-0439-4S (8912003-00-08)	D

SCHEMATIC DIAGRAM



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			SPEC	IFICA	ATION CONTE	ROL DRAWI	NG
DRAWN MGH	DATE 97.11 11 22	SIZE	CODE IDENT. NO.	DWG. N	١0.		REV
CAD FILE	<i>tyco</i>	Α	(06090 D-500-0255-533-1-N				E
C070083B Electronics		DO N	OT SCALE THIS DRA	WING		SHEET 2 OF	4

PRE-SHIPMENT TESTING: TO BE PERFORMED ON 100% OF COMPONENTS.

3.0 VISUAL CHECK

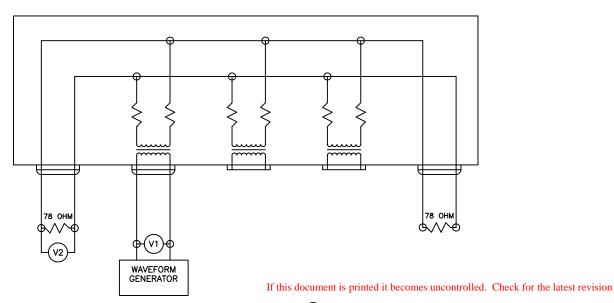
3.1 VISUAL CHECK SHALL BE PERFORMED PER RAYCHEM SPECIFICATION D-6021 AFTER ALL TESTS.

4.0 FUNCTIONAL TEST

- 4.1 COUPLER ASSEMBLY MAXIMUM VOLTAGE TEST SHALL BE PERFORMED USING THE TEST CONFIGURATION SHOWN BELOW. WHEN OUTPUT VOLTAGE IS MEASURED AT V2, THE VOLTAGE SHALL BE WITHIN THE LIMITS OF 5 TO 10 VOLTS AT THE PEAK—TO—PEAK VALUE OF LINE VOLTAGE. THIS TEST SHALL BE PERFORMED BY APPLYING A SQUARE WAVE FORM V1, WHICH ALLOWS PEAK—TO—PEAK VALUE OF 27V AND 250KHz WITH THE SAME 100 NANOSECOND RISE AND FALL TIME AS ABOVE. (TO BE PERFORMED ON ALL STUB LINES.)
- 4.2 COUPLER ASSEMBLY MINIMUM VOLTAGE TEST SHALL BE PERFORMED USING THE TEST CONFIGURATION SHOWN BELOW. WHEN OUTPUT VOLTAGE IS MEASURED AT V2, THE VOLTAGE SHALL BE WITHIN THE LIMITS OF 5 TO 10 VOLTS AT THE PEAK-TO-PEAK VALUE OF LINE VOLTAGE. THIS TEST SHALL BE PERFORMED BY APPLYING A SQUARE WAVE FORM V1, WHICH ALLOWS PEAK-TO-PEAK VALUE OF 18V AND 250KHz WITH THE SAME 100 NANOSECOND RISE AND FALL TIME AS ABOVE. (TO BE PERFORMED ON ALL STUB LINES.)

FIG-1 FUNCTION TEST CONFIGURATION

TWO STUB COUPLER SCHEMATIC



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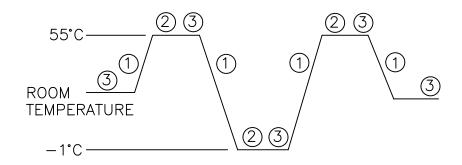
SPECIFICATION CONTROL DRAWING

5.0 RANDOM VIBRATION TESTING SHALL BE PERFORMED IN ACCORDANCE WITH MIL—STD—202, METHOD 214, FOR 60 SECONDS IN EACH OF 3 MUTUALLY PERPENDICULAR AXES USING THE SPECTRUM SHOWN BELOW. THE COUPLER ASSEMBLY SHALL SHOW NO VISIBLE EVIDENCE OF FUNCTIONAL DAMAGE, NO ELECTRICAL SHORT CIRCUITS AND NO ELECTRICAL DISCONTINUITIES GREATER THAN 1 MICROSECOND DURING THE TEST. FUNCTIONAL TESTING SHALL BE PERFORMED AFTER THE TEST FOR EACH AXIS.

FREQUENCY (Hz)	POWER SPE (g²Hz)	CTRAL DENSITY (m/s²)²/Hz	RAMP RATE (dB/OCTAVE)
20 20–40	0.00236	0.2265	+18
40-135 135-140	0.1507	14.49	–12
140-370	0.1306	12.56	. —
370-446 446-630	0.0703	6.764	-10
630-1205 1205-2000	0.0100	0.9663	-9

OVERALL VIBRATION LEVEL: 9.5 G rms

6.0 THERMAL CYCLING TESTING SHALL BE PERFORMED IN ACCORDANCE WITH MIL—STD—202, METHOD 107, USING THE TEST CONDITIONS SHOWN BELOW. THE COUPLER ASSEMBLY SHALL SHOW NO VISIBLE EVIDENCE OF FUNCTIONAL DAMAGE, NO ELECTRICAL SHORT CIRCUITS AND NO ELECTRICAL DISCONTINUITIES GREATER THAN 1 MICROSECOND DURING THE TEST.



- 1) RAMP RATE: 0.5° TO 1.0°C/MINUTE.
- (2) MAINTAIN TEMPERATURE NOT LESS THAN 1 HOUR AFTER STABILIZATION.
- (3) PERFORM FUNCTIONAL TEST AT THE END OF EACH TEMPERATURE DWELL.

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			SPEC	CIFICA	NOITA	CONTR	ROL DI	RAV	<u> </u>	٧G
DRAWN MGH	97JUN3	SIZE	CODE IDENT. NO.	DWG.	NO.					REV
CAD FILE	tuco	1 A	06090	D-5	500-C)255-5	33-1-	-MH	 	Ε
C070083D	Floctmaics	DO N	INT SCALE THIS DR	AWING			SHFFT	1 1	L ^F	1