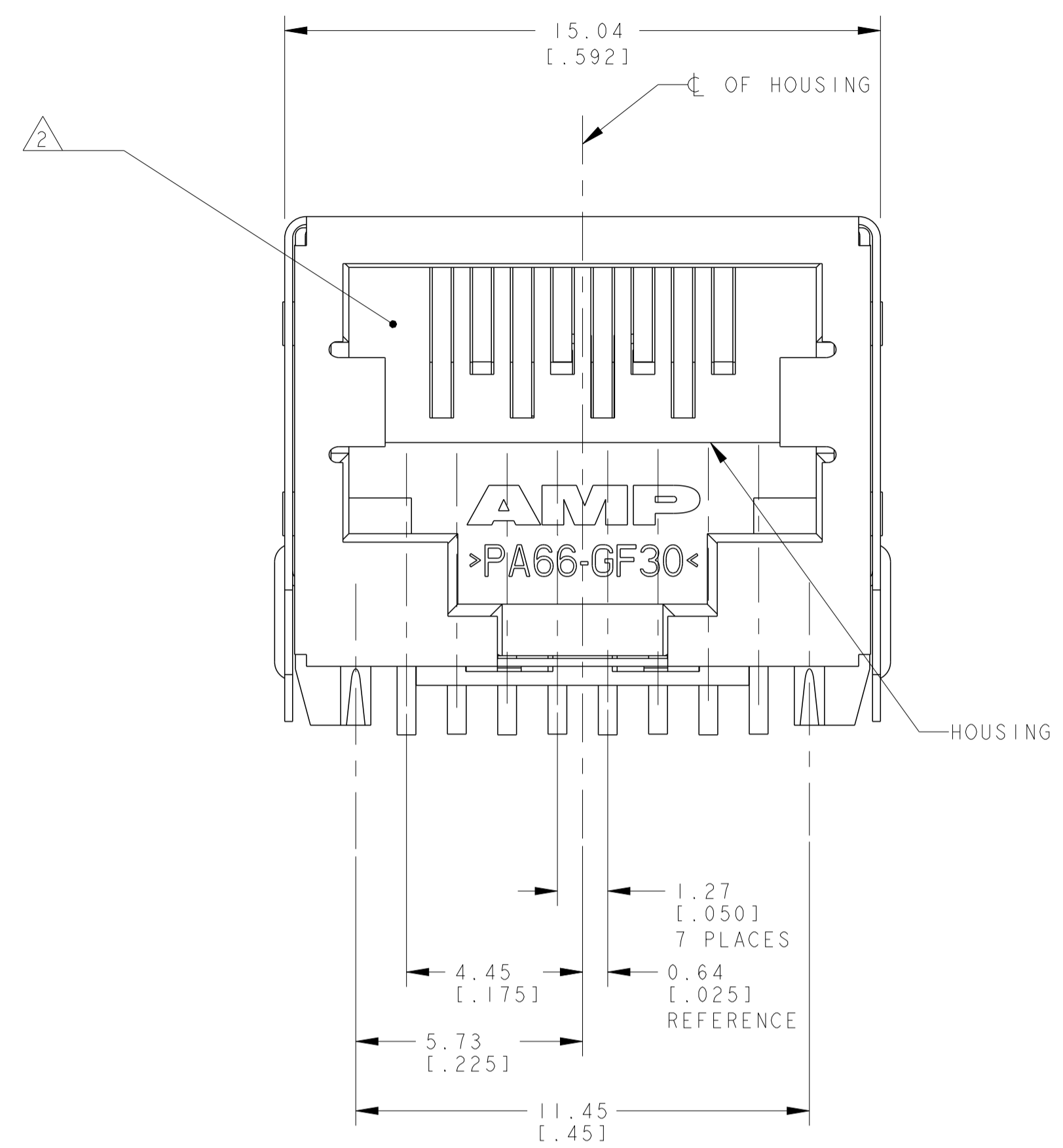
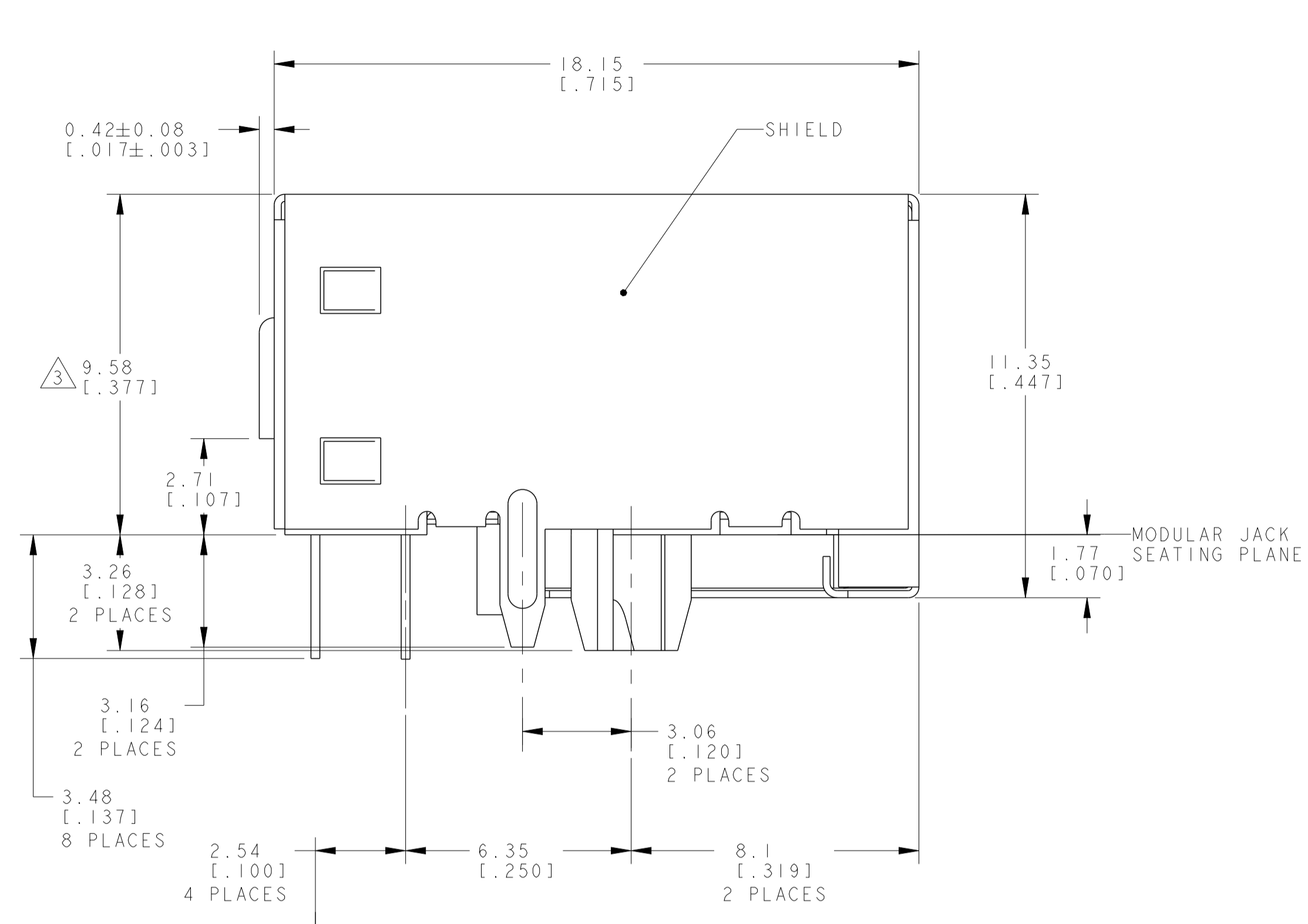
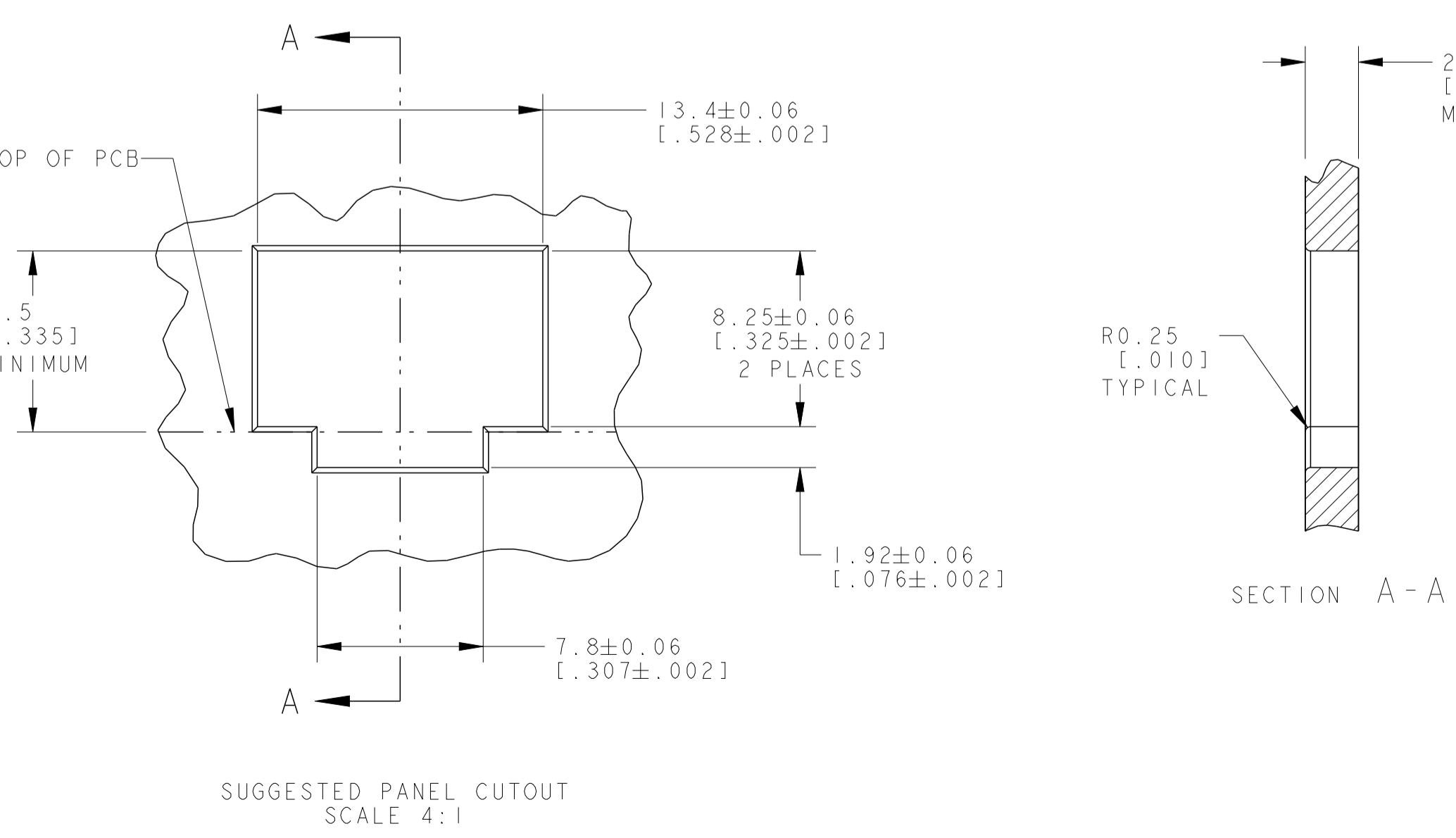
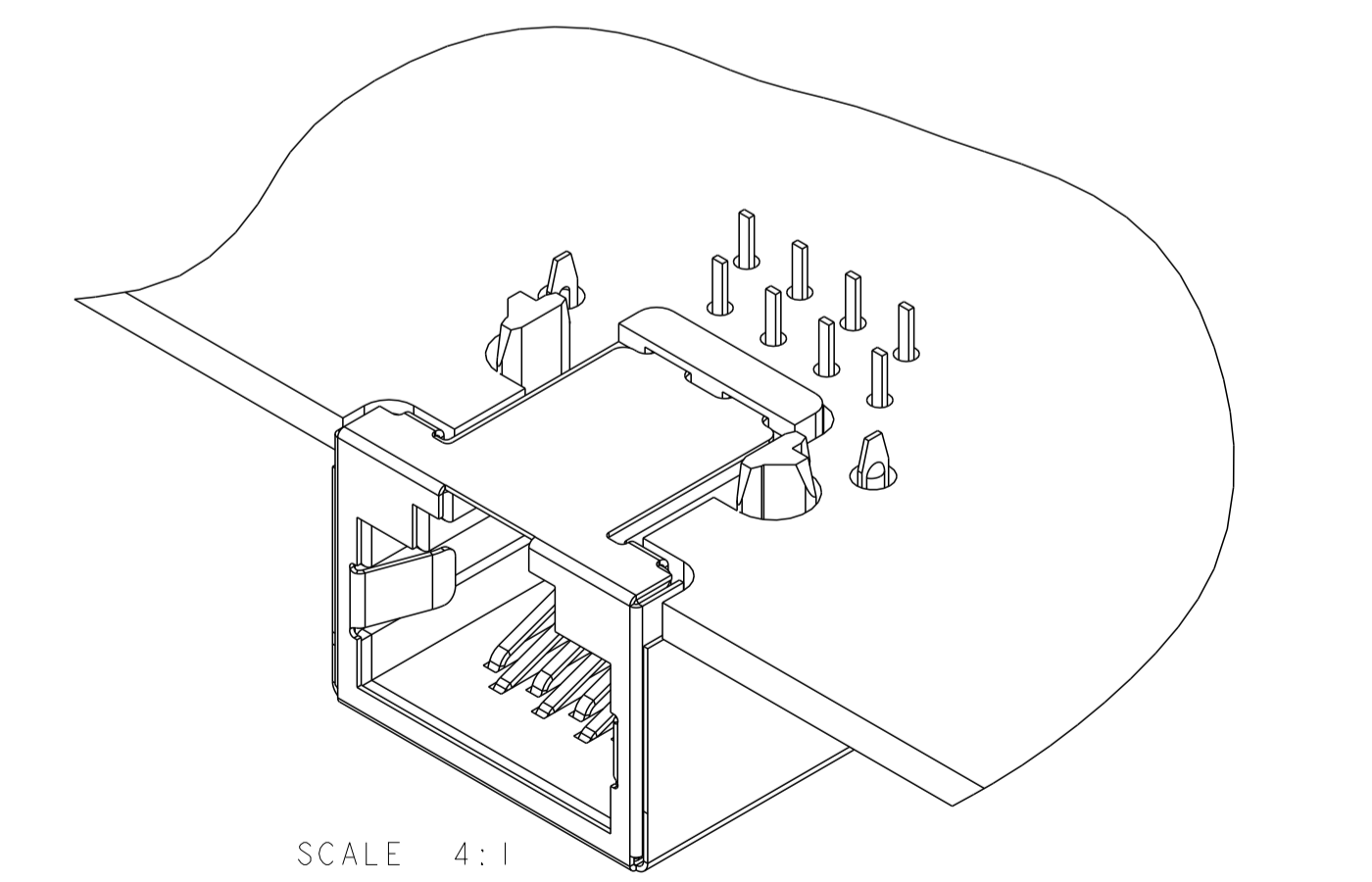
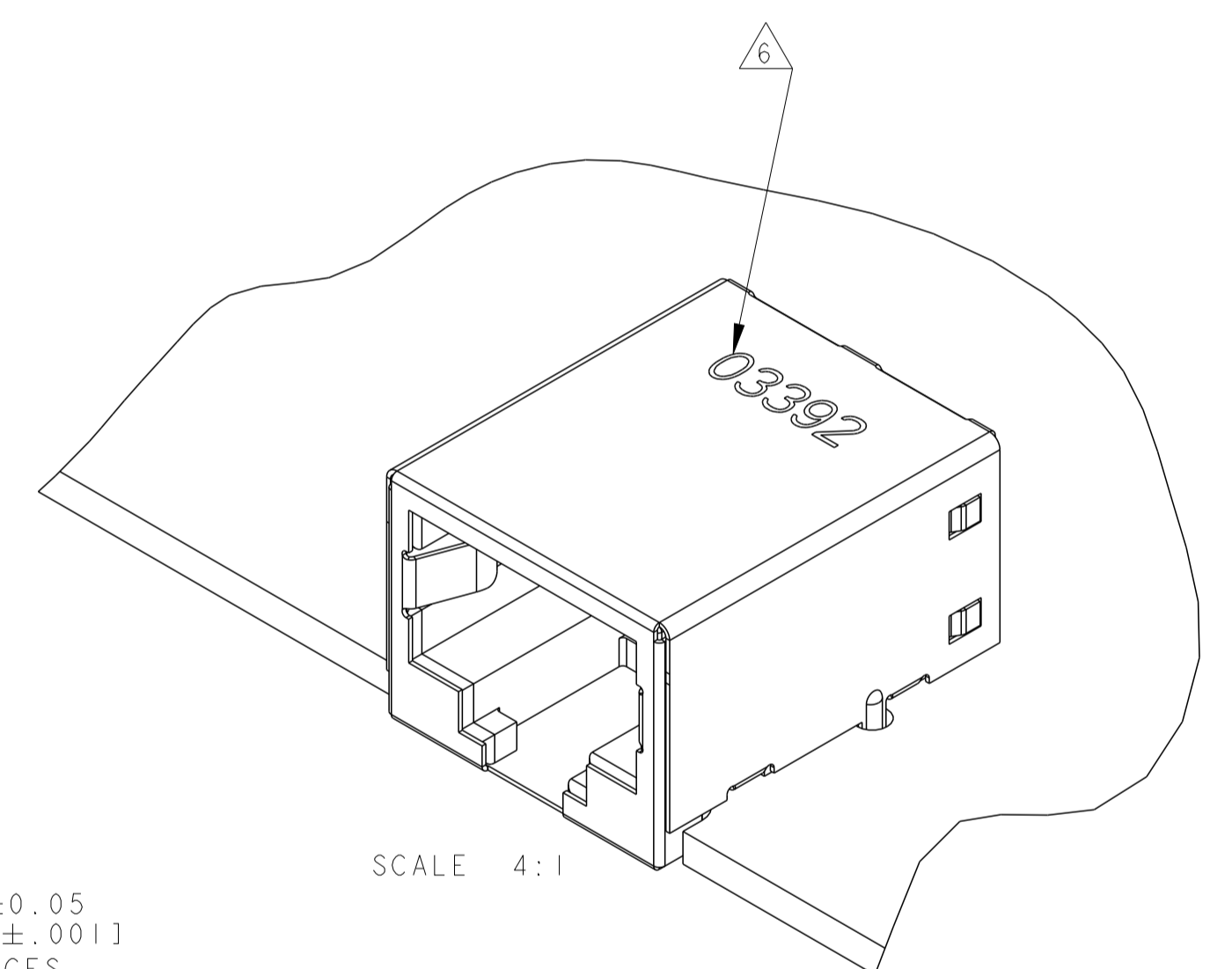
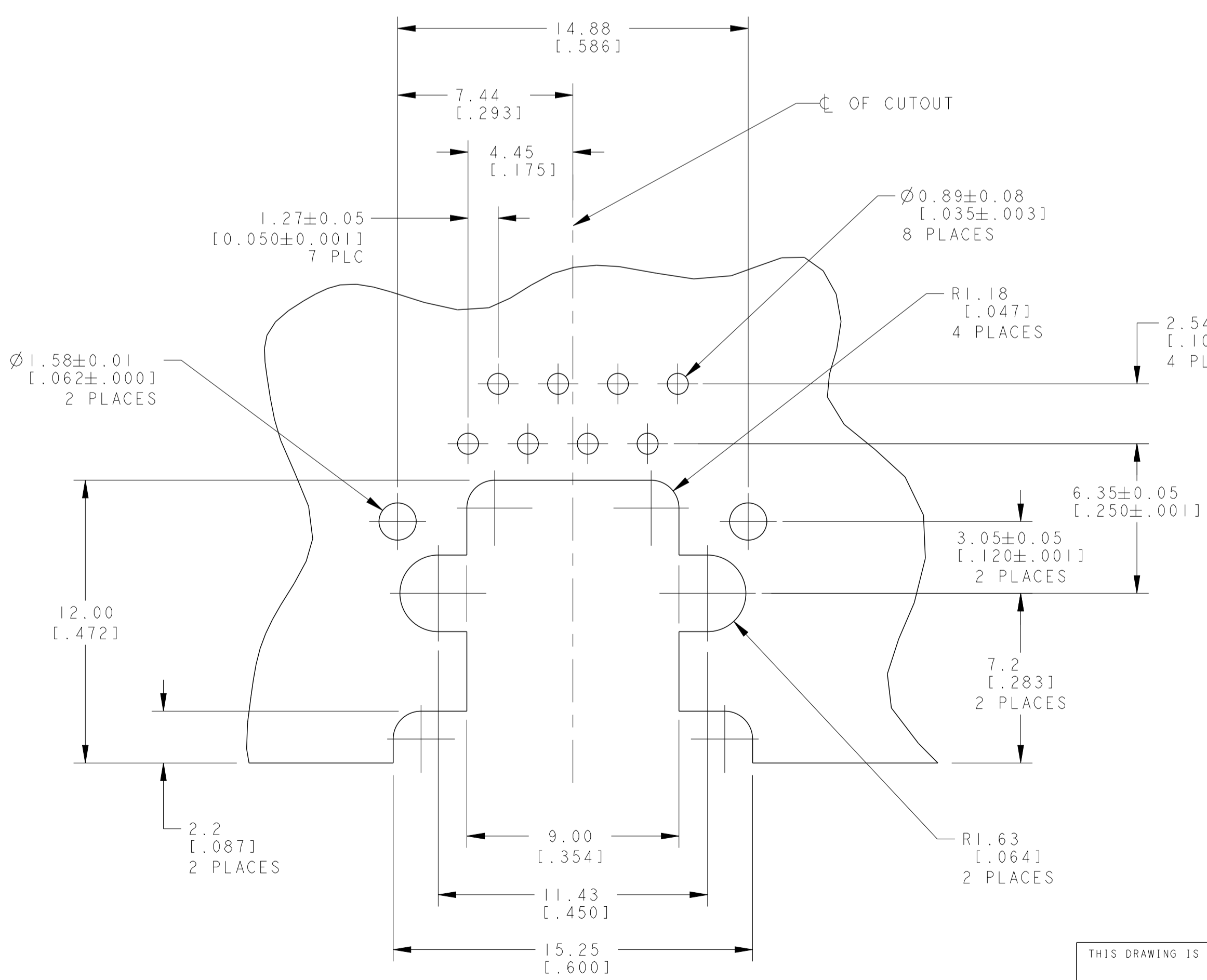
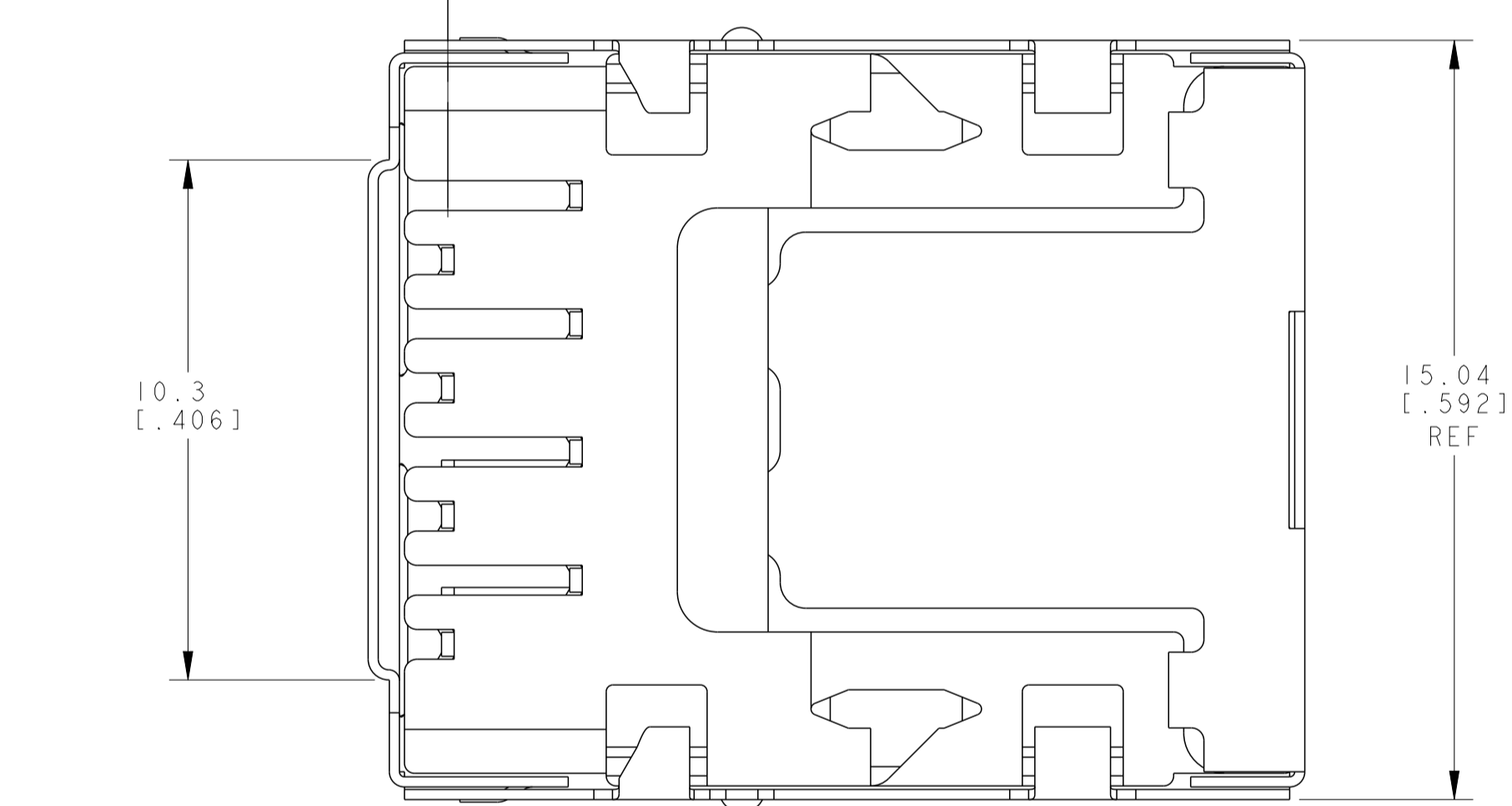


REVISIONS				
P	LTN	DESCRIPTION	DATE	APVD
B		REVISE PER ECO-14-015233	31MAR2015	LL SH
BI		MODIFIED AS PER ECR-23-169440	05APR2023	KV WH



- MATERIAL: HOUSING - HIGH TEMPERATURE NYLON, BLACK, UL 94V-0, IR REFLOW SOLDERING PROCESS COMPATIBLE  
 TERMINALS - 0.25[.01] THICK PHOSPHOR BRONZE PLATED WITH 3.81µm[.000150] MINIMUM THICK MATTE TIN IN SOLDER AREA AREA, 1.27µm[.000050] MINIMUM GOLD IN LOCALIZED PLATE AREA. ENTIRE TERMINAL PLATED WITH 1.27µm[.000050] MINIMUM THICK NICKEL.  
 SHIELD - 0.1[.0039] MIN THICK COPPER ZINC ALLOY, PREPLATED WITH 1.27µm[0.000050]min THICK NICKEL AND HOT TIN DIP ON PCB GROUND TABS.
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F.
- THIS DIMENSION REPRESENTS THE TOTAL HEIGHT OF THE CONNECTOR FROM THE TOP OF THE PC BOARD.
- PACKAGED 66 ASSEMBLIES PER PVC TRAY, 396 PER BOX.
- DIELECTRIC WITHSTANDING VOLTAGE BETWEEN SHIELD AND TERMINAL IS 1500 VAC
- MANUFACTURING DATE CODE:  
 ORIENTED AND LOCATED APPROXIMATELY AS SHOWN. LASER PRINTING.  
 TEXT HEIGHT APPROXIMATELY 2MM.  
 FIRST 2 DIGITS = LAST 2 DIGITS OF YEAR  
 NEXT 2 DIGITS = MANUFACTURING WORK WEEK  
 LAST DIGIT = DAY OF WEEK WITH SUNDAY = 1



SUGGESTED PRINTED CIRCUIT BOARD LAYOUT  
 COMPONENT SIDE  
 SCALE 6:1

1888543-2  
 PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: J. AHERON / J. A. MAYER 07JUNE2006	TE Connectivity	
DIMENSIONS: mm [INCHES]		CHK: J. WESTMAN 07JUNE2006	APVD: S. FLICKINGER 07JUNE2006	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME: MODULAR JACK ASSEMBLY, 8 POSITION, SHIELDED, 10mm WITHOUT PANEL TABS		
0 PLC ±	1 PLC ±	PRODUCT SPEC: 108-1163		
2 PLC ±	3 PLC ±	APPLICATION SPEC: 114-2048		
4 PLC ±	ANGLES ±	SIZE: CAGE CODE DRAWING NO: 1888543		
MATERIAL: SEE NOTE 1		FINISH: SEE NOTE 1		RESTRICTED TO: A1
WEIGHT: 3.49 grams		CUSTOMER DRAWING: SCALE 4:1 SHEET 1 OF 1 REV B1		