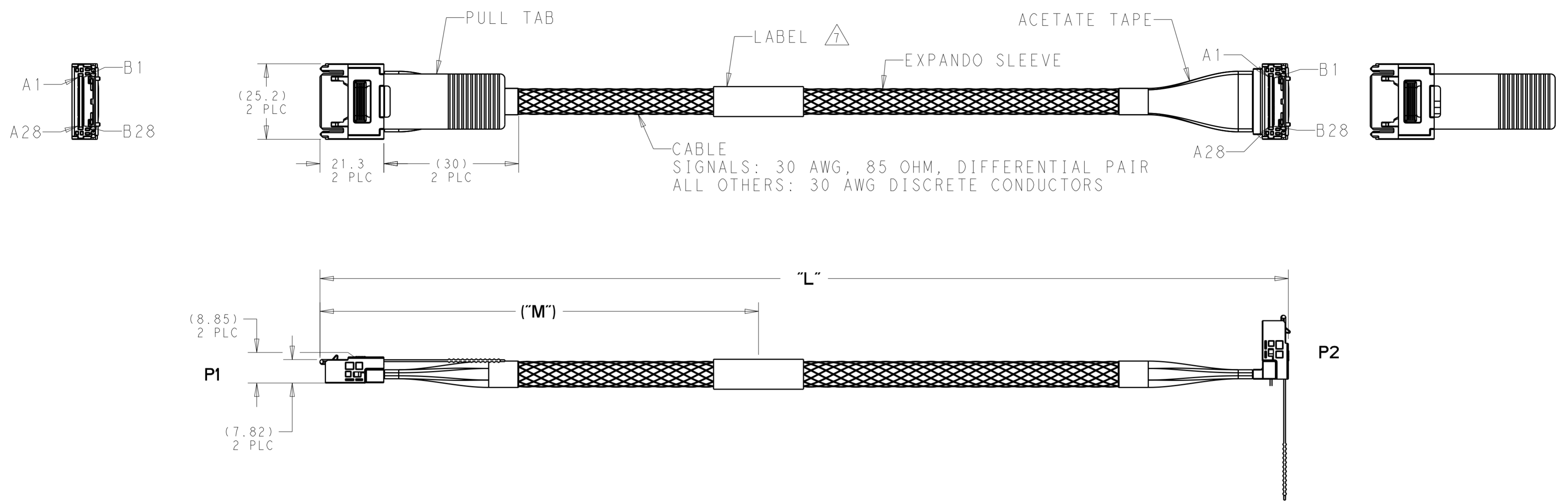


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REVISIONS				
P	LTR	DESCRIPTION	DATE	APVD
1		INITIAL RELEASE	23AUG2019	BM RH



1. ALL MATERIALS, COMPONENTS AND PROCESS SHALL COMPLY WITH TEC-138-702. (CONTAINS NO BANNED OR RESTRICTED SUBSTANCES).
2. NO REACH SvHC SHALL BE CONTAINED ABOVE THE THRESHOLD AS DEFINED IN REACH SvHC COMPLIANCE DEFINITION IN ANNEX A OF TEC-138-702.
3. ASSEMBLY TESTED FOR CONTINUITY, OPENS, SHORTS, AND SIGNAL INTEGRITY.
4. CABLE BEND RADIUS 5X BUNDLED CABLE OD.
5. SEE SHEET 2 AND 3 FOR WIRING SCHEMATIC.
6. CONNECTORS ARE GEN-Z COMPLIANT.

7 LABEL INFORMATION SHOWN BELOW:



8. CABLE CONNECTOR MATES WITH TE SLIVER RECEPTACLE P/N 2332141-X, OR 2332208-X.

500	1000 +10/-0	2361333-2
250	500 +10/-0	2361333-1
("M")	"L"	TE P/N

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN B. MATTHEWS 23AUG2019	TE Connectivity	
DIMENSIONS: mm 		CHK R. HENRY 23AUG2019		
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES ±- FINISH -		APVD -	PRODUCT SPEC -	RESTRICTED TO -
MATERIAL -		APPLICATION SPEC -	WEIGHT -	SIZE A2
CUSTOMER DRAWING		SCALE 1:1	SHEET 1 OF 3	REV 1


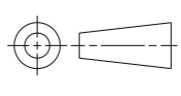
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REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	SEE SHEET 1	-	-	-

P1	WIRE TYPE	P2	
		DESIGNATION	PAIR #
PIN # PAIR # DESIGNATION		DESIGNATION	PAIR # PIN #
A1	DISCRETE	GROUND	A1
A2	DISCRETE	GROUND	A2
A3	DISCRETE	GROUND	A3
A4	DISCRETE	GROUND	A4
A5	DISCRETE	GROUND	A5
A6	DISCRETE	GROUND	A6
A7	DISCRETE	SIDE BAND	A7
A8	DISCRETE	SIDE BAND	A8
A9	DISCRETE	SIDE BAND	A9
A10	DISCRETE	SIDE BAND	A10
A11	DISCRETE	SIDE BAND	A11
A12	DISCRETE	SIDE BAND	A12
A13		GND	A13
A14	DIFF PAIR	CLK	A14
A15	DIFF PAIR	CLK	A15
A16		GND	A16
A17	DIFF PAIR	PCIeTX 0	A17
A18	DIFF PAIR	PCIe TX 0	A18
A19		GND	A19
A20	DIFF PAIR	PCIeTX 1	A20
A21	DIFF PAIR	PCIe TX 1	A21
A22		GND	A22
A23	DIFF PAIR	PCIeTX 2	A23
A24	DIFF PAIR	PCIe TX 2	A24
A25		GND	A25
A26	DIFF PAIR	PCIeTX 3	A26
A27	DIFF PAIR	PCIe TX 3	A27
A28		GND	A28

COMMONED


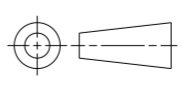
COMMONED

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN B. MATTHEWS 23AUG2019	 TE Connectivity
		CHK R. HENRY 23AUG2019	
DIMENSIONS: mm	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD -	NAME SLIVER 2.0 1C 30AWG STRAIGHT TO RIGHT ANGLE
	0 PLC ± 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES ± FINISH	PRODUCT SPEC -	SIZE A200779
MATERIAL -		APPLICATION SPEC -	CAGE CODE C-2361333
		WEIGHT -	DRAWING NO -
		CUSTOMER DRAWING	RESTRICTED TO -
		SCALE 1:1	SHEET 2 OF 3
			REV 1

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REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	SEE SHEET 1	-	-	-

PIN #	PAIR #	DESIGNATION	WIRE TYPE	P2		
				DESIGNATION	PAIR #	PIN #
B1		12V POWER	DISCRETE	12V POWER		B1
B2		12V POWER	DISCRETE	12V POWER		B2
B3		12V POWER	DISCRETE	12V POWER		B3
B4		12V POWER	DISCRETE	12V POWER		B4
B5		12V POWER	DISCRETE	12V POWER		B5
B6		12V POWER	DISCRETE	12V POWER		B6
B7		SIDE BAND	DISCRETE	SIDE BAND		B7
B8		SIDE BAND	DISCRETE	SIDE BAND		B8
B9		SIDE BAND	DISCRETE	SIDE BAND		B9
B10		SIDE BAND	DISCRETE	SIDE BAND		B10
B11		3.3 AUX POWER	DISCRETE	3.3 AUX POWER		B11
B12		SIDE BAND	DISCRETE	SIDE BAND		B12
B13		GND		GND		B13
B14		CLK	DIFF PAIR	CLK		B14
B15		CLK	DIFF PAIR	CLK		B15
B16		GND		GND		B16
B17	0	PCIeRX	DIFF PAIR	PCIeRX	0	B17
B18	0	PCIe RX	DIFF PAIR	PCIe RX	0	B18
B19		GND		GND		B19
B20	1	PCIeRX	DIFF PAIR	PCIeRX	1	B20
B21	1	PCIe RX	DIFF PAIR	PCIe RX	1	B21
B22		GND		GND		B22
B23	2	PCIeRX	DIFF PAIR	PCIeRX	2	B23
B24	2	PCIe RX	DIFF PAIR	PCIe RX	2	B24
B25		GND		GND		B25
B26	3	PCIeRX	DIFF PAIR	PCIeRX	3	B26
B27	3	PCIe RX	DIFF PAIR	PCIe RX	3	B27
B28		GND		GND		B28

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN B. MATTHEWS 23AUG2019	 TE Connectivity	
DIMENSIONS: mm 		CHK R. HENRY 23AUG2019		
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES ±- FINISH -		APVD -	NAME SLIVER 2.0 1C 30AWG STRAIGHT TO RIGHT ANGLE	
MATERIAL -		PRODUCT SPEC -	APPLICATION SPEC -	RESTRICTED TO -
-		WEIGHT -	SIZE A2	CAGE CODE 00779
-		CUSTOMER DRAWING	DRAWING NO. C-2361333	SCALE 1:1 SHEET 3 OF 3 REV 1